

IN THE CLAIMS

Claim 1 (currently amended): A peak detector for detecting a peak signal, comprising:

an input circuit to input an input signal;

a track and hold circuit to hold said input signal and to output said peak signal;

a comparator to compare said input signal and said peak signal to generate a clock signal;

said track and hold circuit to input said clock signal and to output said peak signal in accordance with said clock signal.

Claim 2 (original): A peak detector for detecting a peak signal as in Claim 1, wherein said peak detector includes a capacitor to hold said peak signal.
